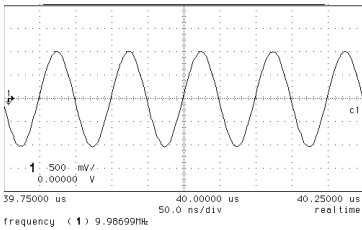


Order PN: 04-000-2233

**OMA1**



## ANALOG

16 channels

Sine Wave Output

40mA continuous drive with up to 150mA peak current

Each channel has independently controlled

- Frequency 10Hz to 20MHz
- Phase 1° granularity
- Amplitude up to +/-10Vpp @ 1MHz, 2vPP @ 20MHz (see chart)
- DC Offset up to +/-10V, Less 1/2 of p-p amplitude of signal

## LVDS

2 M-LVDS channels, per MLVDS specifications

The last four analog channels (13-16) are shared with the LVDS channels

- Amplitude MLVDS standard range and offset
- Frequency 10Hz to 25MHz Clock source
- Phase 1° granularity

## FLEXIBLE CONFIGURATION

Channels are switched in 8 groups of 2 channels (1-4) (5-8) (9-12) (13-16)

Configuration of signal sources to BIB during downloading of LOT

- Std Driver logic channels from S81 - S96 (module not selected)
- Analog Channels (1-16) assigned to S81 - S96
- LVDS channels (1-2) assigned to S93 - S96 (1 LVDS = 2 Channels)



**INCAL TECHNOLOGY**

46420 Fremont Blvd.  
Fremont, CA 94538  
Phone: (510) 657-8405  
[www.incal.com](http://www.incal.com)

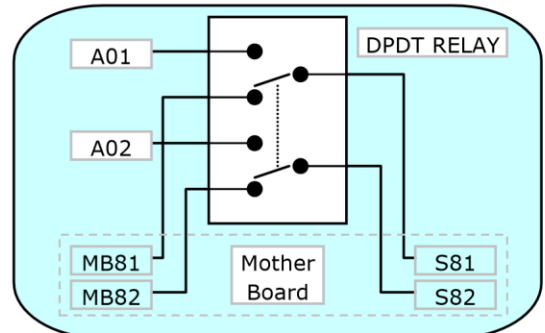
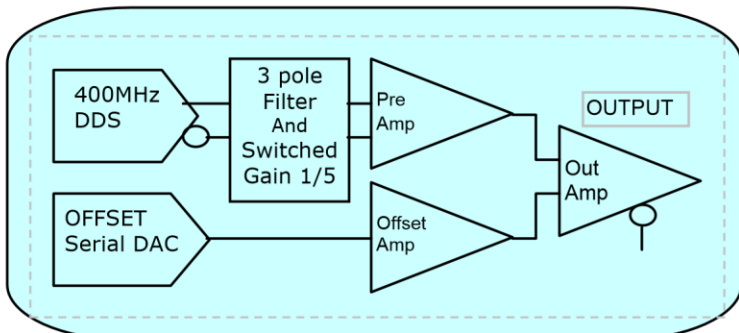
**NOTE:**

Advanced training and signal transmission considerations are important for a successful product application.

Application notes and Burn-in board design guidelines are available for these application capabilities with consideration on signal quality limitations.

Specification updated 1/29/2008

# OMA1 Block Diagram



Basic Hardware Accuracy  
Accuracy to set value

## Sine Wave Channels

### Frequency

The greater of 0.2% or 20Hz

### Phase

5° shift

### Amplitude

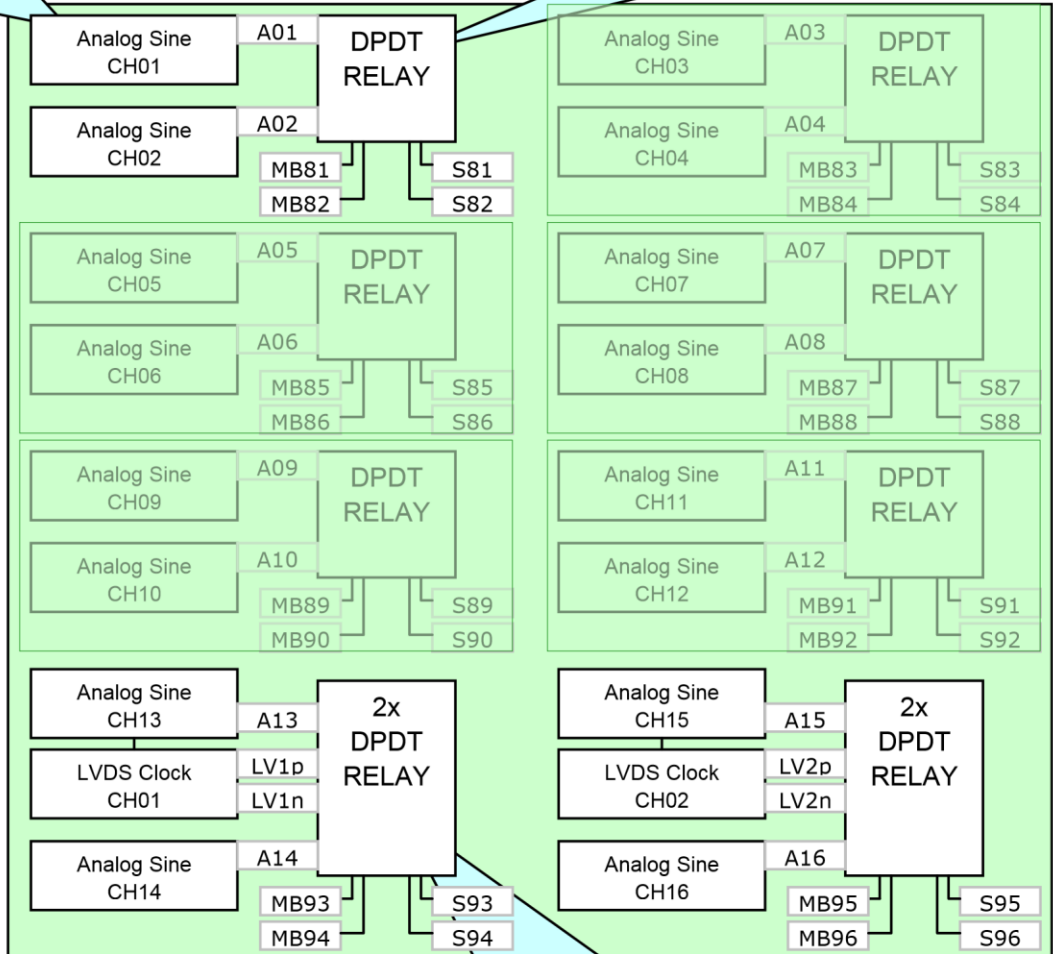
5% FS or

+/- 250mV < 2vpp

+/- 350mV > 2vpp

### Dc Offset

+/-150mV



## LVDS Channels

### Frequency

The greater of 0.2% or 20Hz

### Phase

5° shift

## Frequency Vs Sine wave Amplitude

